Low-Noise Phase-Locked Loop Extends to 5 GHz

This integer-N frequency synthesizer builds on silicon CMOS semiconductor technology to achieve low-noise signals to 5 GHz from a compact, radiation-hardened CQFP package.

FREQUENCY SYNTHESIZERS ARE widely used both in ground and in space, where they must remain tolerant to different types of radiation. A new solution from Peregrine Semiconductor, the model PE97240, is a radiation-hardened integer-N phase-locked loop (PLL) that is offered in a compact surface-mount package capable of generating low-noise signals through 5 GHz. In addition to achieving low phase noise, the device uses very low power, typically drawing only 75 mA current from a +2.7-V dc supply. It is designed for use at operating temperatures from -40 to +85°C.

The model PE97240 PLL (*see the fig-ure*) is fabricated on the company's proven

UltraCMOS semiconductor technology, which features siliconon-insulator (SOI) circuitry on a sapphire substrate. Much like the firm's model PE42020 single-pole, double-throw (SPDT) surface-mount switch introduced one month earlier (see *Microwaves & RF*, February 2015, p. 82), this device benefits from the linearity and low noise of this semiconductor process. The PE97240 PLL was designed for commercial space applications, featuring radiation tolerance to 100 krad total ionizing dose (TID) and excellent immunity to single-event-effect (SEE) phenomena. With such specifications, this device will be able to perform reliably for 10 years or more in the harsh radiation conditions of space.

The PLL includes a prescaler with selectable 5/6 or 10/11 dual-modulus operation. The compact 44-lead ceramic-quad-flat-pack (CQFP) package also includes a reference divider, counters, phase comparator, and control logic. The counter values can be programmed by means of a serial interface or under direct-wired control. The dual-modulus prescaler divides an input from a voltage-controlled oscillator (VCO), and the counters work with the control logic and phase-frequency detector to generate control signals to stabilize output signals over the respective ranges of each division ratio.

Model PE97240 is an integer-N frequency synthesizer that features low noise performance to 5 GHz.

PE97240

When used with the 5/6 dual modulus, the PE97240 provides an output frequency range of 800 MHz to 4 GHz, with maximum input power of +7 dBm. When used with the 10/11 dual modulus, the PLL operates at frequencies from 800 MHz to 5 GHz with maximum input power of +7 dBm. The device works with a phasedetector comparison frequency to 100 MHz. This space-ready PLL boasts very good spectral purity, with low phase noise when using either dual modulus. For example, with the 5/6 dual modulus, the typical single-sideband uen- (SSB) phase noise measured for a 3-GHz car-

rier is –100 dBc/Hz at 100-Hz offset from the carrier; –109 dBc/Hz at 1-kHz offset from the carrier; –116 dBc/Hz at 10-kHz offset from

the carrier; and -118 dBc/Hz at 100-kHz offset from the carrier. When using the 10/11 dual modulus, the SSB phase noise is typically -98 dBc/Hz at 100-Hz offset from the carrier; -104 dBc/Hz at 1-kHz offset from the carrier; -111 dBc/Hz at 10-kHz offset from the carrier; and -117 dBc/Hz at 100-kHz offset from the carrier.

The model PE97240 PLL synthesizer's hermetic CQFP package is RoHS-compliant. To assist specifiers interested in incorporating the device in their system, Peregrine offers an evaluation board with a Universal Serial Bus (USB) interface that helps demonstrate the optimum phase-noise performance of the PLL.

This evaluation board is fabricated with high-frequency 4350B printed-circuit-board (PCB) material from Rogers Corp. (www.rogerscorp.com) with relative dielectric constant (ϵ_r) of 3.48 and two inner layers of FR-4 PCB material. The board is well grounded for reliable performance when investigating the performance of the PE97240 PLL under the types of conditions that may be experienced in space-based applications.

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